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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,684	02/20/2004	Hai-Zhi Song	040070	3691
23850	7590	11/17/2005	EXAMINER	
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP 1725 K STREET, NW SUITE 1000 WASHINGTON, DC 20006			DICKEY, THOMAS L	
			ART UNIT	PAPER NUMBER
			2826	

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/781,684

Applicant(s)

SONG, HAI-ZHI

Examiner

Thomas L. Dickey

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 September 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) 12-16 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-9 and 11 is/are rejected.
- 7) ☒ Claim(s) 10 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 20 February 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

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DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 09/29/2005 has been entered.

Oath/Declaration

2. The oath/declaration filed on 02/20/2004 is acceptable.

Drawings

3. The formal drawings filed on 02/20/2004 are acceptable.

Priority

4. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

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Information Disclosure Statement

5. The Information Disclosure Statement filed on 02/20/2004 has been considered.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-9 and 11 are rejected under 35 U.S.C. 102(b) as being anticipated by JAIN ET AL. (6,498,360).

With regard to claims 1-9 Jain et al. discloses a quantum semiconductor device with a first semiconductor layer 20 formed on a substrate 10 and having a two-dimensional carrier gas 19 formed in; a quantum dot 25 (quantum dot 25 and dot-shaped structure 23 are referred to generically as "coupled sub-wells," but claimed specifically as quantum dots in claim 25) formed on the first semiconductor layer 20; a second semiconductor layer 24 formed on the first semiconductor layer 20 and covering the quantum dot 25; a dot-shaped structure 23, being another (coupled") quantum dot, formed on the surface of the second semiconductor layer 24 at a position above the quantum dot 25, source 14/drain 15 regions connected to both ends of a channel region

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defined by depletion regions formed in a regions of the first semiconductor layer 20 which are below oxide layers 21 and 22 formed on both sides of the dot-shaped structure 23 on the upper surface of the second semiconductor layer 24; and a gate electrode 17 connected to the dot-shaped structure 23; wherein the thickness of second semiconductor layer 24 (which separates quantum dot 25 and dot-shaped structure 23) is 15 angstroms, thus defining a distance between the two-dimensional carrier gas and the quantum dot 25 of 5 nm (50 angstroms) or less. Note figures 2,8,9, column 3 lines 15-20,53-54, column 4 lines 17-20, and column 6 lines 25-29 and 53-54 of Jain et al.

The applicant's claims 2-5 does not distinguish over the Jain et al. reference regardless of the process used to form the quantum dot 25 and dot-shaped structure 23, because only the final product is relevant, not the recited processes of forming the dot-shaped structure by generating crystal strains in the surface of the second semiconductor layer, or self-assembling the quantum dot and the dot-shaped structure by S-K mode.

Note that a "product by process" claim is directed to the product per se, no matter how actually made. In re Hirao, 190 USPQ 15 at 17 (footnote 3). See also In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and In re Marosi et al., 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by

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process” claim and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in “product by process” claims or not. Note that applicant has the burden of proof in such cases, as the above caselaw makes clear. See also MPEP 706.03(e).

With regard to claim 11, which is a necessary method of making the quantum semiconductor device of claim 1, Jain et al. discloses a method of making said quantum semiconductor device comprising the steps of forming on a substrate 10 a first semiconductor layer 20 with a two-dimensional carrier gas 19 formed in; forming a quantum dot 25 on the first semiconductor layer 20; forming a second semiconductor layer 24, covering the quantum dot 25; forming a dot-shaped structure 23 on the surface of the second semiconductor at a position above the quantum dot 25 due to strains generated in the surface of the second semiconductor layer 24 due to the presence of the quantum dot 25; and forming oxide layers 21 and 22 on both sides of the dot-shaped structure 23 on the upper surface of the second semiconductor layer 24. Note figures 2,8,9, column 3 lines 15-20,53-54, column 4 lines 17-20, and column 6 lines 25-29 and 53-54 of Jain et al.

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Allowable Subject Matter

7. Claim 10 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

1. Applicant's arguments with respect to claims 1-9 and 11 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas L Dickey whose telephone number is 571-272-1913. The examiner can normally be reached on Monday-Thursday 8-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 571-272-1915. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For

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more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'T. Dickey', is positioned above the printed name.

Thomas L. Dickey
Patent Examiner
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11/05